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Flash memory device employing unused cell arrays to update files

RW DeMarco, PAA Dempsey... - US Patent 5,566,314, 1996 - Google Patents ... In addition, in the event of a power outage, the microprocessor will be able to **determine** which memory is current based on the higher sequence number. ... Memory 10 can then be erased for subsequent reuse. Memory 20 can then be used in place of memory 10 until ...

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Method and apparatus for allocating shared memory resources among a plurality of queues each

having a threshold value therefor

N Yin - US Patent 6,219,728, 2001 - Google Patents

... Another feature of the invention performs a comparison of the threshold value with the queue usage to determine whether to accept or discard incoming data cells destined for the queue. ... Referring to FIG. 6A, usage of shared memory 102 has increased in comparison to ...

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The SDSS imaging pipelines

R Lupton, JE Gunn, Z Ivezic, GR Knapp... - Arxiv preprint astro-ph/ ..., 2001 - arxiv.org

... Figure 1 shows that the total memory used in the steady state by the frames pipeline (see below) is well ... This consists of a set of staring-mode observations of fields of standard stars, used to define the ... there are typically 15-25 stars in a frame that we can use to determine the PSF ...

Cited by 149 - Related articles - BL Direct - All 13 versions

[PS] A memory allocator

D Lea... - 2000 - mit.edu

... mmap, the call itself can be expensive, so is only attempted if trailing unused memory exceeds a ... Additionally, effectiveness less obviously depends on the policy used in deciding when to cache versus ... For example, it is just as costly for an allocator to determine how much total ...

Cited by 156 - Related articles - View as HTML - All 3 versions

Evaluating the effects of SEUs affecting the configuration memory of an SRAM-based FPGA

M Bellato, P Bernardi, D Bortolato... - Proceedings of the ..., 2004 - portal.acm.org ... In other words, these bits determine how ... Conflict: a new PIP called Net_X links an input and an output node, both used, as shown in Fig. ... the output node; None: the PIP configuration is not affected by the fault that modified an unused portion of the device configuration memory; ...

Cited by 63 - Related articles - All 13 versions

Distributed processing memory chip with embedded logic having both data memory and broadcast

memory

SS Maharit-Shetti, DJ Smith, Bi Pawate... - US Patent ..., 1998 - Google Patents ... US005751987A [ii] Patent Number: [45] Date of Patent: 5,751,987 May 12, 1998 [54]

DISTRIBUTED PROCESSING **MEMORY** CHIP WITH EMBEDDED LOGIC HAVING **BOTH** DATA **MEMORY** AND BROADCAST **MEMORY** [75] Inventors: Shivaling S. Mahant-Shetti, Richardson ...

Cited by 65 - Related articles - All 2 versions

The memory fragmentation problem: solved?

MS Johnstone. . - ACM SIGPLAN Notices, 1999 - portal acm.org

... With this information, and the maximum number of live bytes for the trace, we can determine the fragmentation, as described in Section 5 for a particular program using ... The upper line is the amount of mem- ory actually used by the allocator to satisfy GCC's memory requests. ...

Cited by 132 - Related articles - Bt. Direct - All 22 versions

Efficient implementation of a BDD package

KS Brace, Rt. Rudell... - ... of the 27th ACM/IEEE design ..., 1991 - portal.acm.org ... The package should also allow a function to be tested for tautology - ie, to determine whether

the function ... A limit of 2 megabytes (Mb:) was placed on the memory usage of the ROBDD package.

A hash-based cache was used for the computed-table, and a load factor of 4 was ...

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Flash memory file caching for mobile computers

B Marsh, F Douglis... - System Sciences, 1994. Vol. I ..., 2002 - jeeexplore.ieee.org

... To evaluate the cache in a steady state, we used a one-week period to "prime" the ... The simulator also keeps track of the state of the disk and the flash memory. ... At appropriate times, it also invokes strategy routines that determine when a device should change state: an example ...

Cited by 98 - Related articles - Bt. Direct - All 4 versions

Page printer memory allocation

JL Beck, JK Brown III, CP Cole... - US Patent ..., 1992 - Google Patents

... 6 is a more detailed flowchart of a routine im-plementing the operation illustrated in FIG. 5;

FIG. 7 illustrates diagrammatically used and unused areas of a user memory and page buffer; FIG. 8 is a flowchart of a subroutine used in the routine of FIG. 6; FIGS. ...

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